

FIG. 1

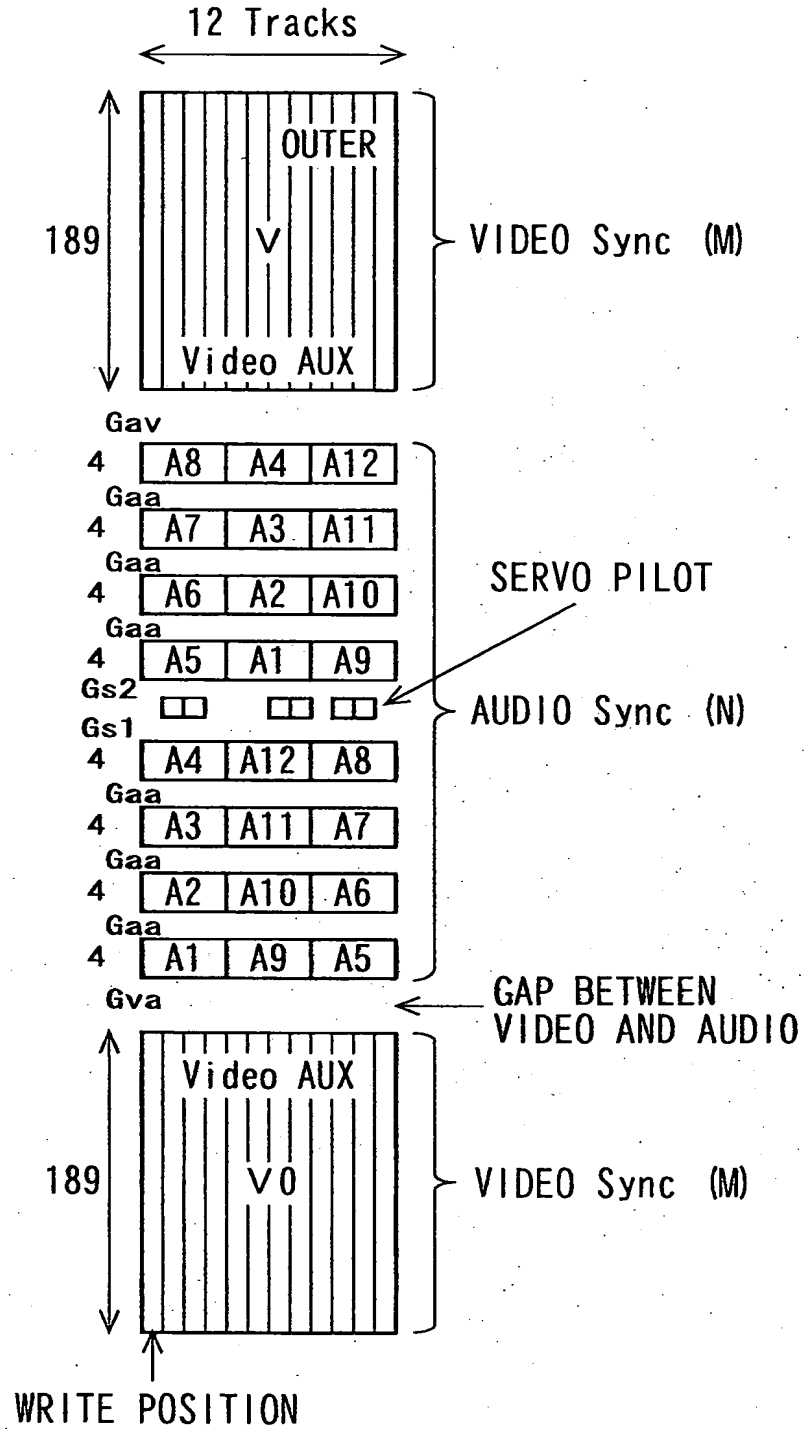


FIG. 2A

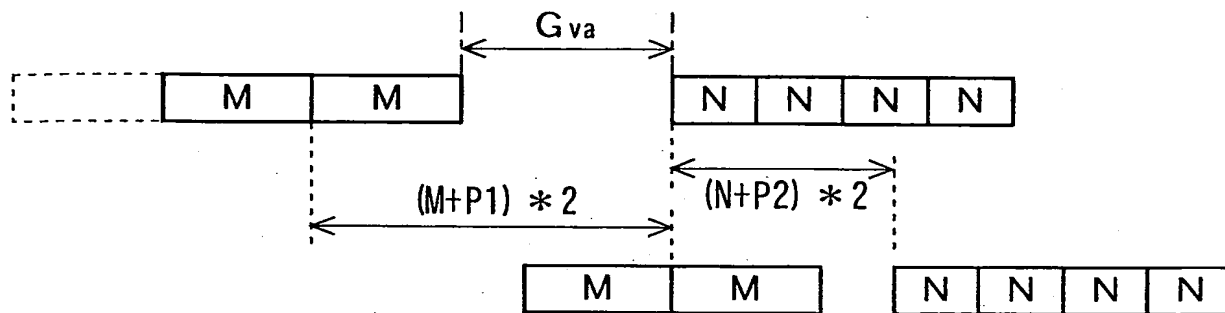
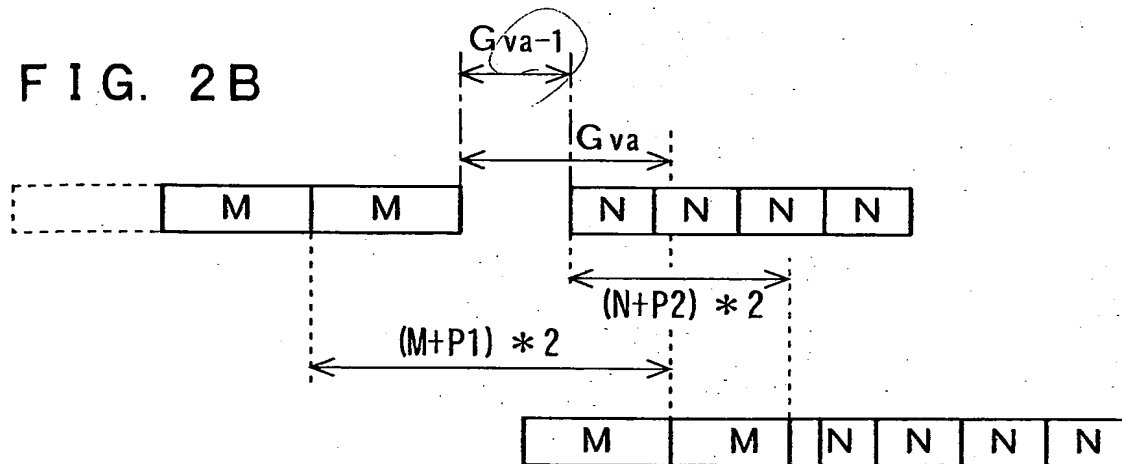


FIG. 2B



BACK OF "M" IS OVERTAKEN AND DESTROYED BY "N"

FIG. 3

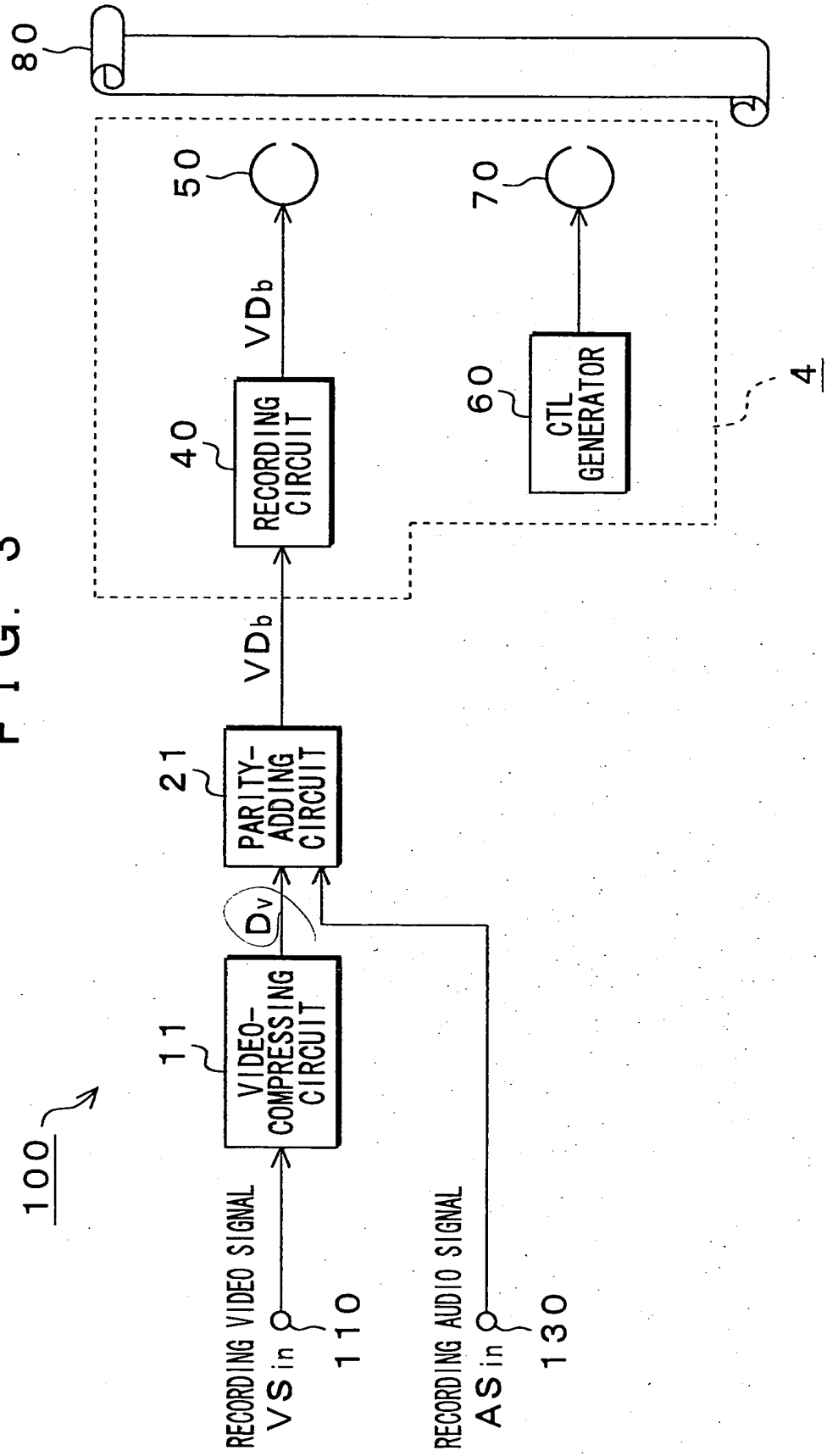


FIG. 4

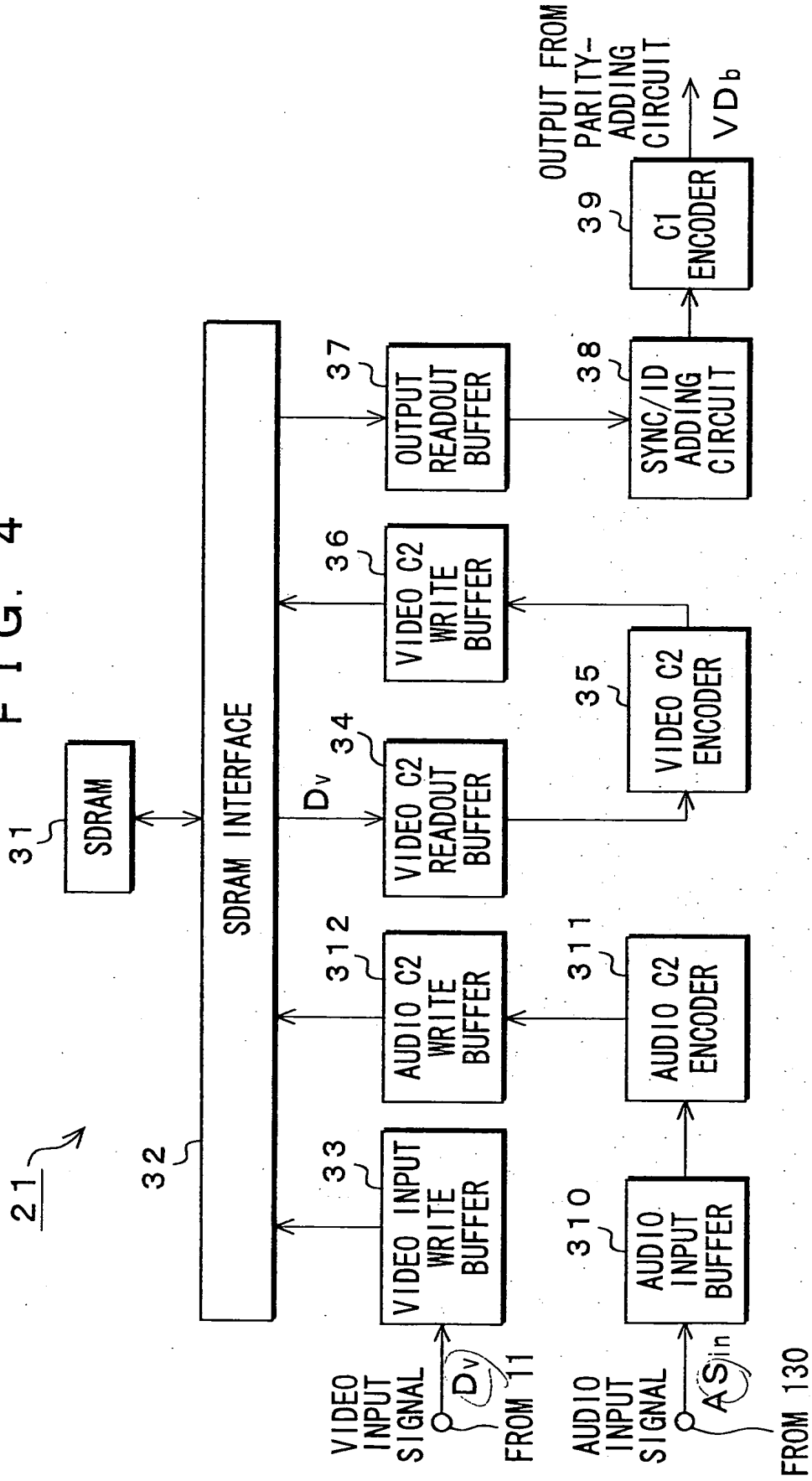
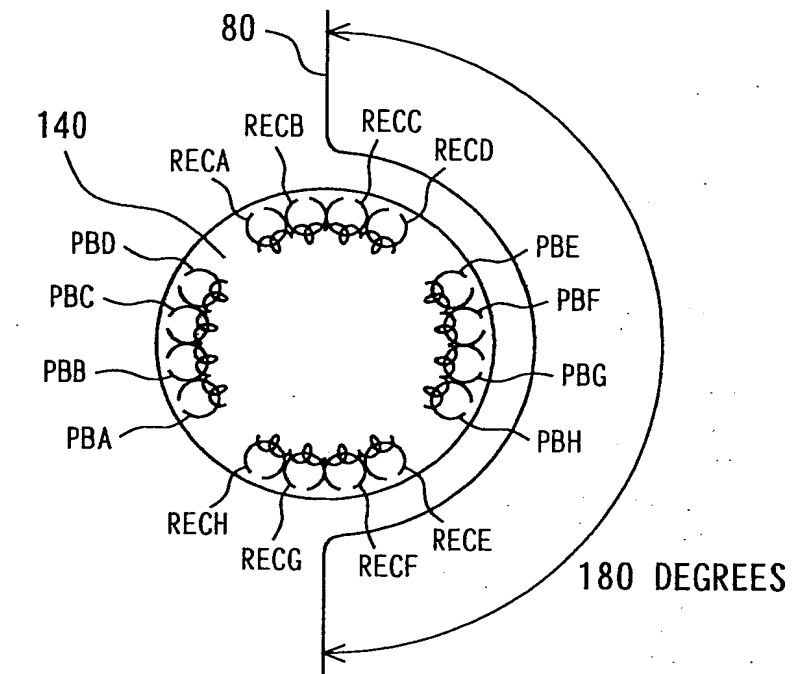


FIG. 5



50 : RECA~RECH

55 : PBA~PBH

FIG. 6

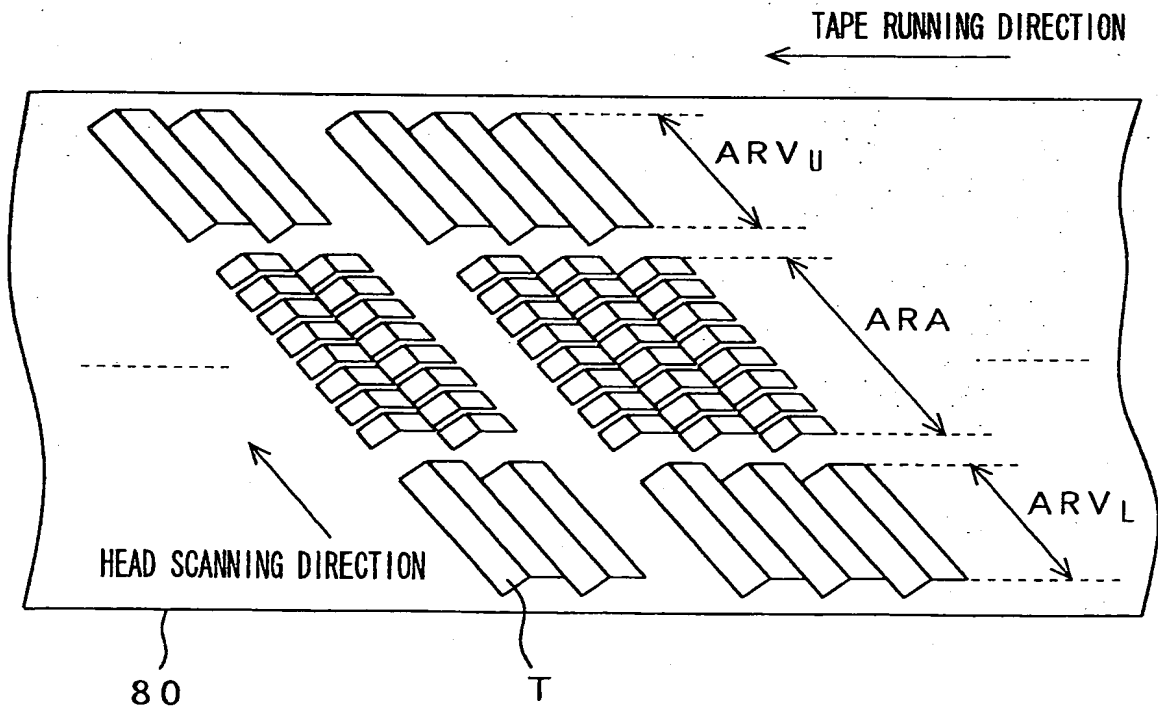
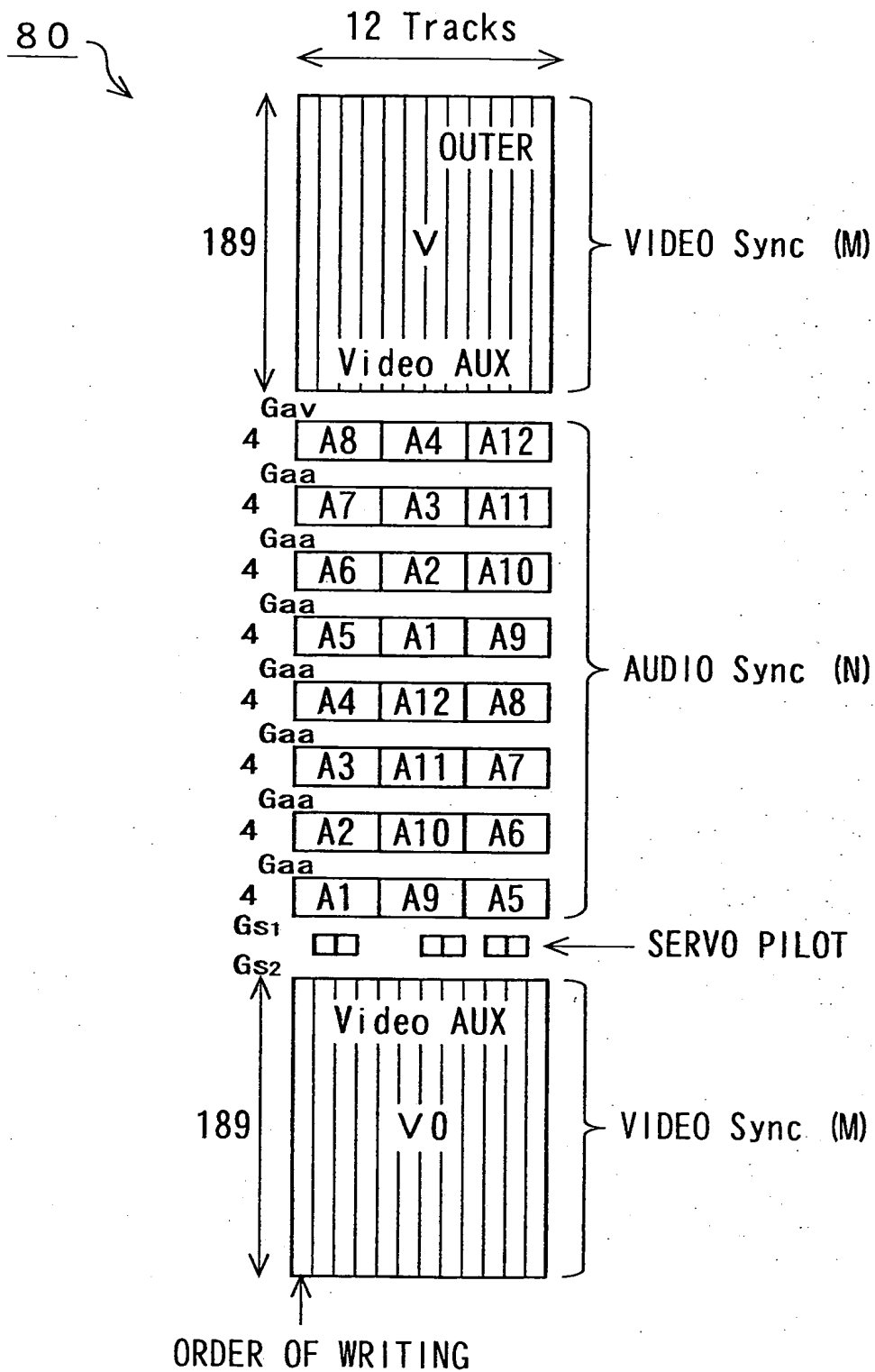
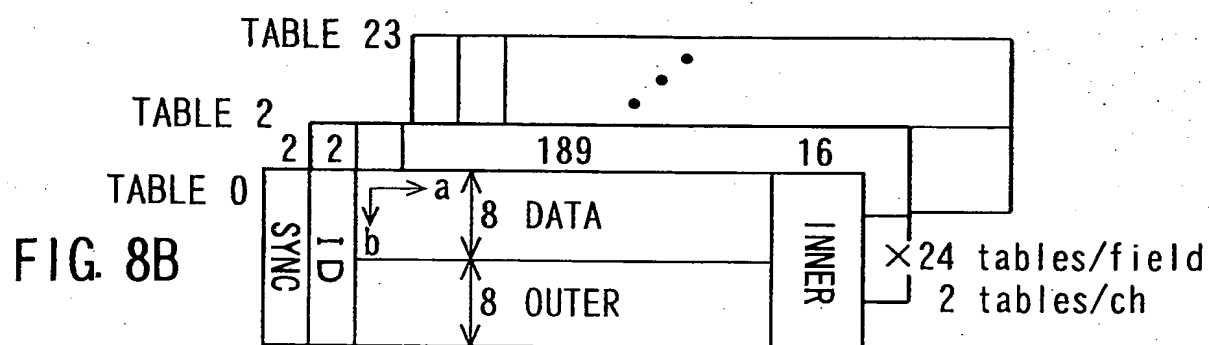
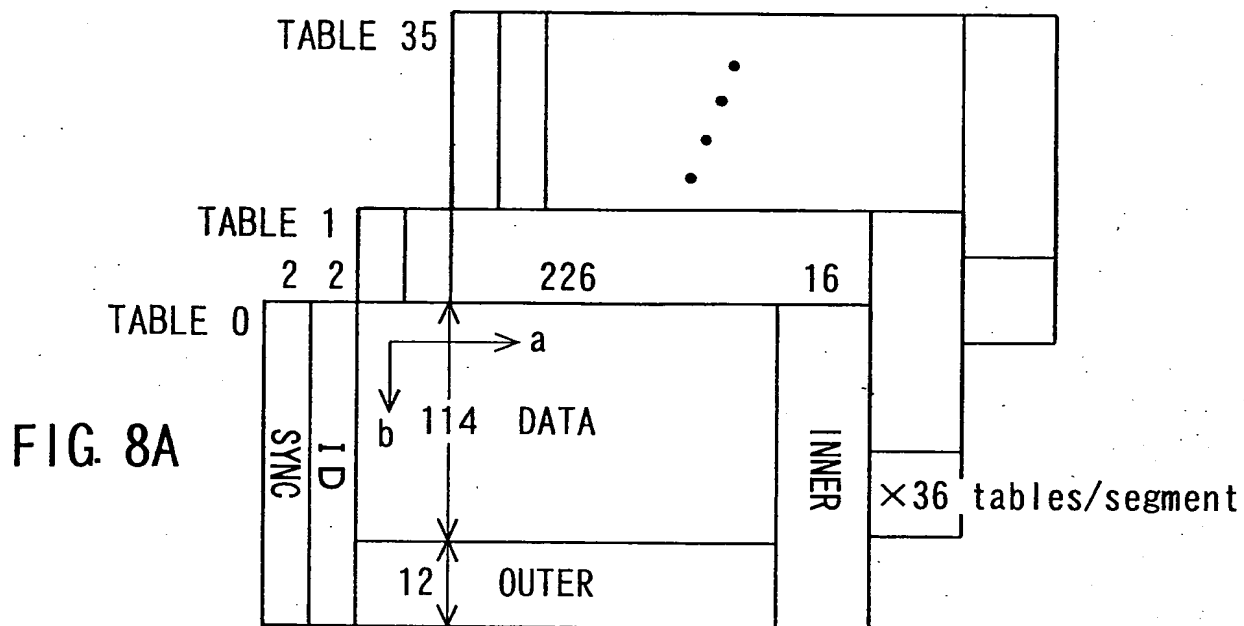


FIG. 7





The diagram illustrates a 2D array structure with 21 rows and 12 tracks. The array is divided into two sections: ARV_U (top) and ARV_L (bottom). The top section (ARV_U) has 10 rows and 12 tracks, with the last row containing values 41, 83, and 125. The bottom section (ARV_L) has 11 rows and 12 tracks, with the last row containing values 20, 62, and 104. The array is labeled with 21 Rows and 12 Tracks.

23	10	32	1
5	27	14	18
22	9	31	0
4	26	13	35
21	8	30	17
3	25	12	34
20	7	29	16
2	24	11	33
19	6	28	15
1	23	10	32
18	5	27	14
0	22	9	31
0	1	2	3

9 SYNC BLOCKS

4 TRACKS

16	2	2	2	2	16
DATA	DATA	DATA	DATA	DATA	DATA
16	2	2	2	2	16

FIG. 10

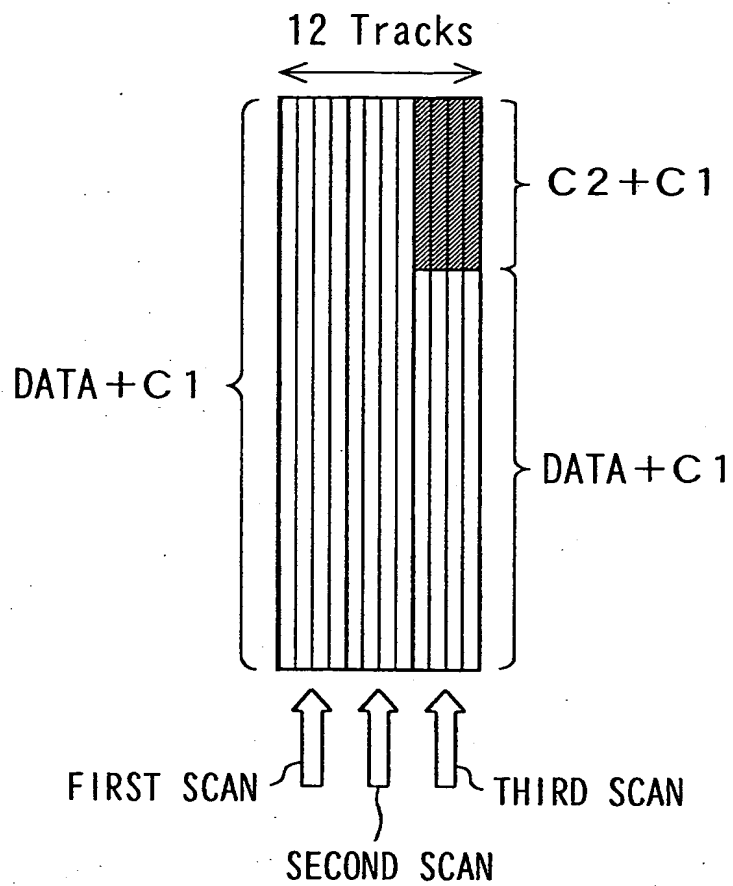


FIG. 11

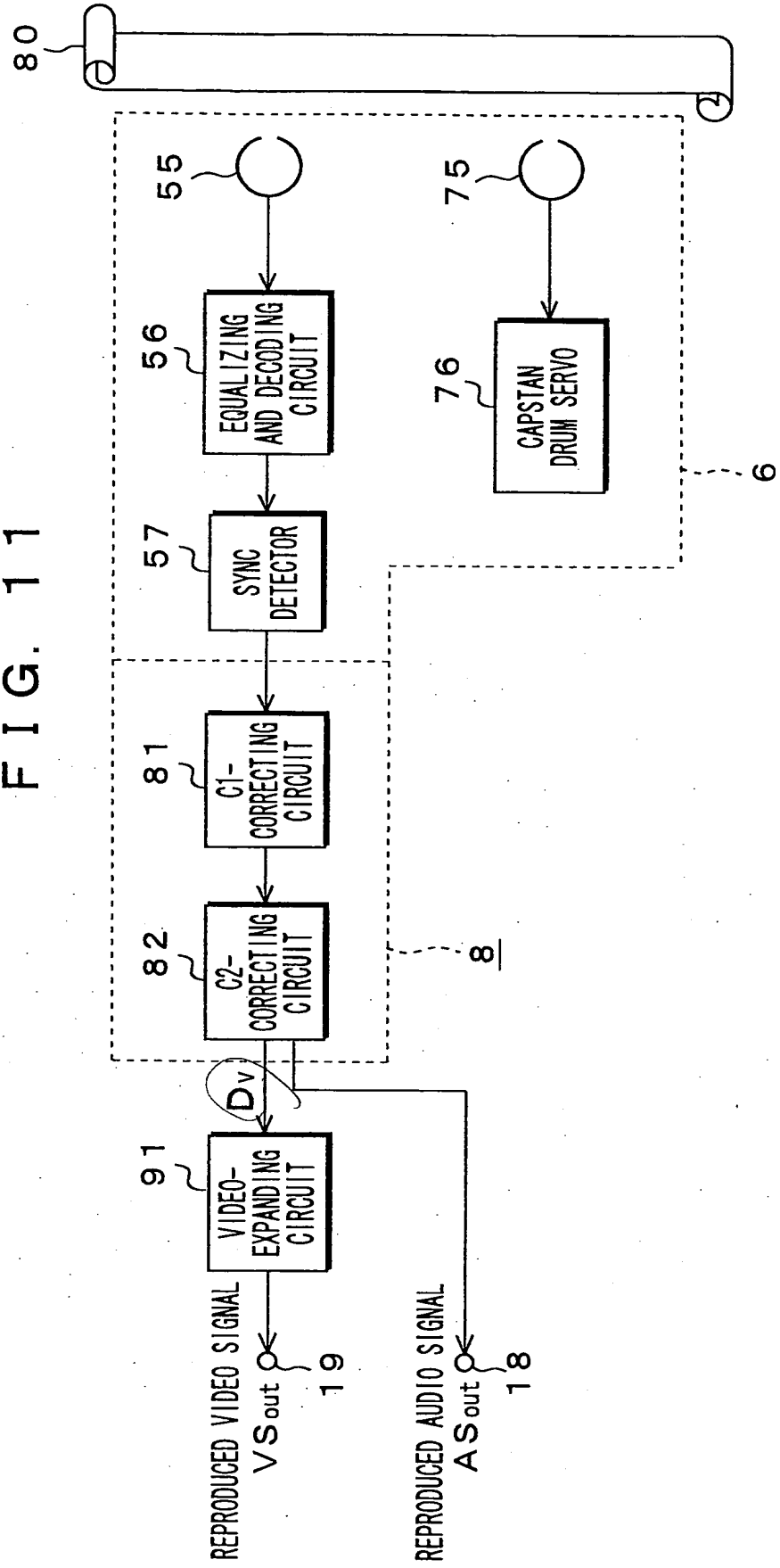
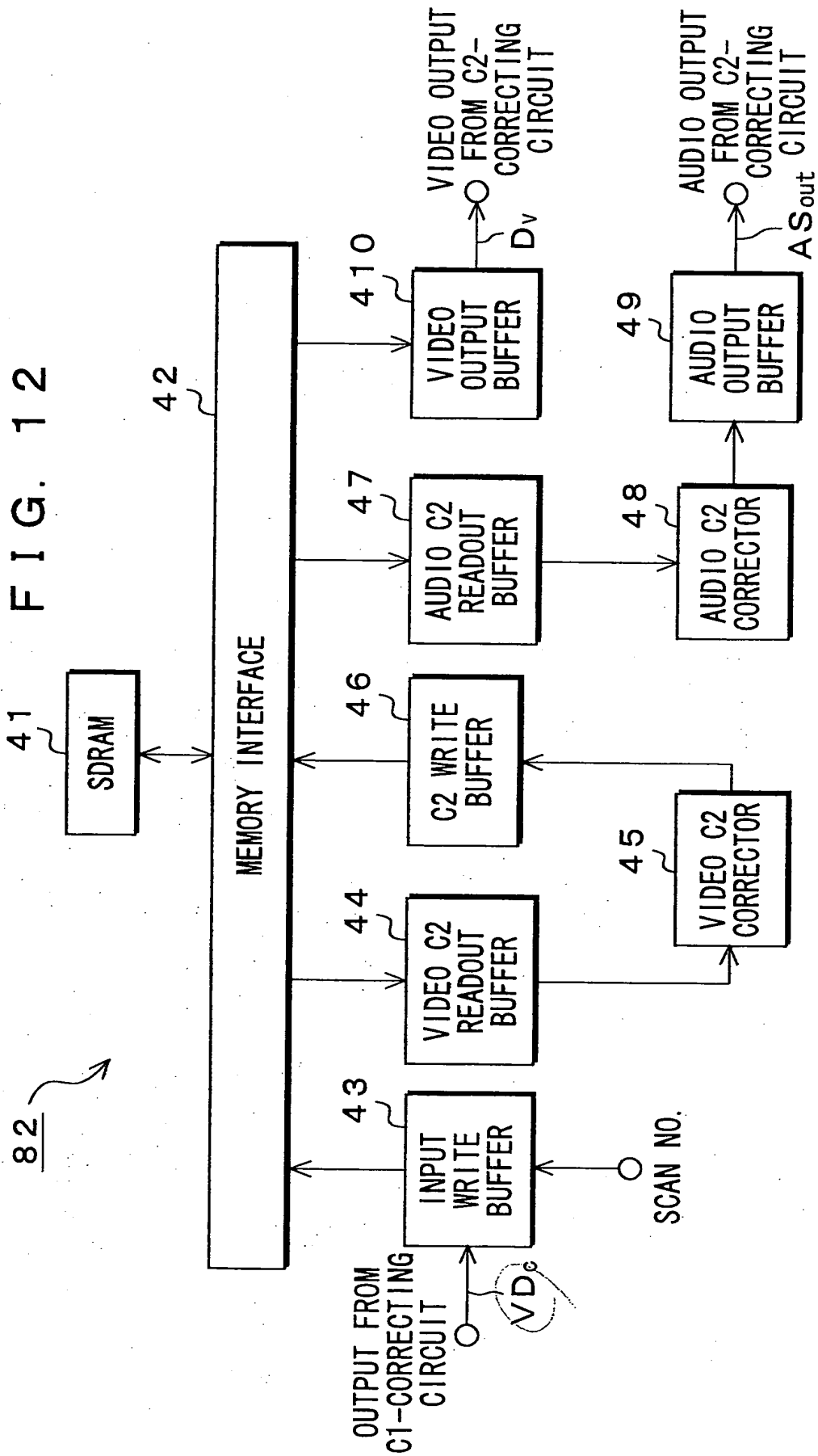
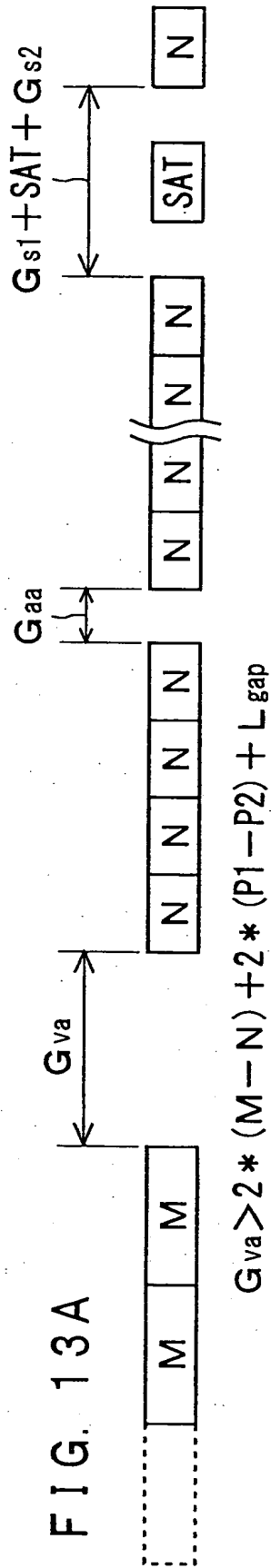


FIG. 12



CONVENTIONAL SYSTEM



SYSTEM ACCORDING TO
THE PRESENT INVENTION

